

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,041,976 B1  
APPLICATION NO. : 10/700144  
DATED : May 9, 2006  
INVENTOR(S) : Neil et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page: Item [75], under inventors, delete

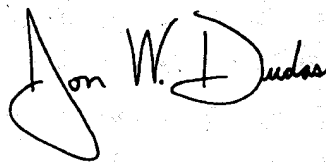
-- **Gabor D. Toth**, San Jose, CA (US); **Varoujan Chakarian**, Fremont, CA (US); **Douglas K. Masnaghetti**, San Jose, CA (US) --

on column 9, line 2, after "stage bias voltage", delete  
-- is decreased --

on column 10, line 3, after "stage bias voltage", delete  
-- is --

Signed and Sealed this

Fifteenth Day of August, 2006

A handwritten signature in black ink, reading "Jon W. Dudas". The signature is stylized, with a large loop for the "J" and a cursive "Dudas".

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*